



October 7, 2004

Shawn D. Jacka B.Sc. (Comp. Sci.), LL.B.
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Our Reference: 7750-113

Honorable Commissioner for Patents
P. O. Box 1450
Alexandria, Virginia 22313-1450

ATTN: WILSON LEE, PRIMARY EXAMINER
ART UNIT: 2821

Dear Sir:

Re: U.S. Patent Application No. 09/986,893
For: SMART LIGHT SOURCE WITH INTEGRATED OPERATIONAL
PARAMETER DATA STORAGE CAPABILITY
Applicant: Roy Kayser

This correspondence is in response to the Notice of Allowability dated September 13, 2004.

The Examiner has requested another copy of the Information Disclosure Statement that was filed with the Response dated August 2, 2002. Accordingly, the Applicant encloses an additional copy of the Information Disclosure Statement (including patents and non-patent references), as well as the Examiner's marked up copy of the Information Disclosure Statement from the parent application no. 09/459,989. The Applicant wishes the prior art references listed on the IDS to be identified on the front page of any patent that may issue.

The Applicant believes that since the Information Disclosure Statement was timely provided to the Patent Office, no fees are required. However, if fees are required, the Examiner is requested to advise the undersigned and charge the fees to our deposit account no. 02-2095.

Respectfully submitted,

Shawn D. Jacka
Registration No. 43,379

/as
Enc.

Please send all correspondence to the Toronto office:

Scotia Plaza, 40 King St. West, 40th Floor,
Toronto, Ontario, Canada M5H 3Y2
Tel: 416.364.7311 Fax: 416.361.1398

2000 Argentia Rd., Plaza 4, Ste. 430,
Mississauga, Ontario, Canada L5N 1W1
Tel: 905.812.3600 Fax: 905.814.0031

Waterloo Technology Campus, 408 Albert St., Ste. 2,
Waterloo, Ontario, Canada N2L 3V3
Tel: 519.783.3210 Fax: 519.783.3211

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**TRANSMITTAL
FORM**

(to be used for all correspondence after initial filing)

Application Number	09/986,893
Filing Date	11/13/2001
First Named Inventor	Roy Kayser
Art Unit	2821
Examiner Name	Wilson Lee
Attorney Docket Number	7750-113

Total Number of Pages in This Submission

ENCLOSURES (check all that apply)

- | | | |
|---|--|---|
| <input type="checkbox"/> Fee Transmittal Form

<input type="checkbox"/> Fee Attached

<input type="checkbox"/> Amendment / Reply

<input type="checkbox"/> After Final

<input type="checkbox"/> Affidavits/declaration(s)

<input type="checkbox"/> Extension of Time Request

<input type="checkbox"/> Express Abandonment Request

<input checked="" type="checkbox"/> Information Disclosure Statement

<input type="checkbox"/> Certified Copy of Priority Document(s)

<input type="checkbox"/> Response to Missing Parts/ Incomplete Application

<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53 | <input type="checkbox"/> Drawing(s)

<input type="checkbox"/> Licensing-related Papers

<input type="checkbox"/> Petition

<input type="checkbox"/> Petition to Convert to a Provisional Application

<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address

<input type="checkbox"/> Terminal Disclaimer

<input type="checkbox"/> Request for Refund

<input type="checkbox"/> CD, Number of CD(s) | <input type="checkbox"/> After Allowance Communication to Technology Center (TC)

<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences

<input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)

<input type="checkbox"/> Proprietary Information

<input type="checkbox"/> Status Letter

<input checked="" type="checkbox"/> Other Enclosure(s) (please identify below):
<p style="text-align: center;">Cover Letter</p> |
|---|--|---|

Remarks

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Bereskin & Parr
-------------------------	-----------------

Signature

Bereskin & Parr

Date

October 7, 2004

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.

Typed or printed name

Signature

Date

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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#2

Form PTO-1449 U.S. DEPARTMENT OF COMMERCE
(REV. 2-32) PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.: 7750-044

SERIAL NO.: 09/459,989

APPLICANTS:

Roy Kayser

INFORMATION DISCLOSURE STATEMENT
BY APPLICANT

FILING DATE: December 14, 1999

GROUP: 2828-2821

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
TK	4,509,508	Apr. 09/85	Tsukaya	128	6	
TK	5,541,490	July 30/96	Sengupta et al.	320	14	
TK	5,561,361	Oct. 01/96	Sengupta et al.	320	14	
TK	5,652,502	July 29/97	van Phuoc et al.	320	30	

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)

TK	1.	System Extension Data Book, pp. 788 - 801, ©1995 - 1996 by Dallas Semiconductor Corporation
TK	2.	Tech Brief No. 1 - MicroLAN Design Guide by Dan Awtrey, dated 2/7/99, http://www.dalsemi.com/TechBriefs/tb1.html
	3.	
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* EXAMINER: Initial if citation considered, whether or not citation is in accordance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Examiner Signature: Wilson Lee Date: August 11, 2000

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

Application Number	09/986,893
Filing Date	11/13/2001
First Named Inventor	Roy Kayser
Art Unit	2821
Examiner Name	Wilson Lee
Attorney Docket Number	7750-113

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
		US- 4,509,508	April 09/85	Tsukaya	
		US- 5,541,490	July 30/96	Sengupta et al.	
		US- 5,561,361	Oct 01/96	Sengupta et al.	
		US- 5,652,502	July 29/97	van Phuoc et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		System Extension Data Book, pp. 788 – 801 ©1995 – 1996 by Dallas Semiconductor Corporation	
		Tech Brief No. 1 – MicroLAN Design Guide by Dan Awtrey, dated 2/7/99, http://www.dalsemi.com/TechBriefs/tb1.html	

Examiner
SignatureDate
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 120 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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